FORM PTO-144? (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 851763.447	APPLICATION NO. 10/776,024
	INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	APPLICANTS Andrea Pagni et al.	
		FILING DATE February 10, 2004	GROUP ART UNIT 2183

(Stamp: OIPE / JUL 0 7 2004 / PATENT & TRADEMARK OFFICE)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
EC	AK	1 028 370 A2	08/16/00	EP		
EC	AL	WO 02/086699 A2	10/31/02	WIPO		
	AM					
	AN					
	AO					

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EC	AP	IBM Corp., "Real-Time CISC Architecture HW Emulator on a RISC Processor," *IBM Technical Disclosure Bulletin 37(3):605, March 1994.*
	AQ	
	AR	

EXAMINER Eric Col	DATE CONSIDERED 4/16/06

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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Date: 07/07/04